

Software Development using Virtual Platforms

Simon Davidmann Imperas CEO OVP founder

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New Tools and Methodology Needed for Embedded Software Development

Multicore

Proliferation





Adoption of new tools to produce higher quality embedded software





Move to Virtual Platforms for Software development





Software Complexity Explosion

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Virtual Platforms Types



- Hardware Virtual Platforms
 - Timing / Cycle accurate
 - Used for architecture performance analysis, drivers, firmware
 - Models are very complex, slow, time consuming to build
 - SystemC etc
 - Main value is same performance as RTL, no Verilog license cost...
- Software Virtual Platforms
 - Instruction accurate
 - Used for OS, applications
 - Can be very fast, programmers views
 - Model only what is needed in peripherals
 - Complete system environment

New SoC Embedded Software Development market



Older approach was to use prototypes, breadboards, FPGA, previous generation - this fails to satisfy for next generation complexity



Virtual Platforms (OVP) are the foundation for the next generation of [embedded] software development environments – especially for multicore





What is in OVP?



Modeling APIs

- Publishing of C OVP APIs for Processor, Peripheral, and Platform modeling
- Documentation & header files

Open Source library of models

- C source of models written to C OVP APIs
- Processor models of ARM, ARC, MIPS, OpenRisc OR1K, x86, …
- Peripheral models of standard embedded devices
- Example embedded platforms in C, C++, SystemC, TLM2.0
 - Including full platforms that boot operating systems like Linux, Nucleus

• OVP reference simulator, free for non-commercial use

- Runs processor models fast, 500 MIPS typical
- Interfaces to GDB via RSP/socket
- MP Capable, scalable and very efficient
- Can encapsulate existing processor models (ISS)
- Callable with C/C++/SystemC wrapper
- Website community/portal
- Ecosystem growing for model and tool developers and users

OVPsim booting uClinux on ARM Atmel AT91sam7





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OVPsim booting Nucleus on ARM Integrator / ARM920



X	(grahar	n@lnx16:~/workspaceArmIntegrator <3>		Open Virtual Platforms
	graham@lnx16:~/workspaceArmIntegrator <3> Task 5 Event Detections: 22 RX Buffer:			Open Virtual Platforms
	Task 5 Event Detections: RX Buffer: Easy to ru	23 <u>n</u>		PIC AHB Decoder UART UART Htroller
tein	net localhost 9999	Keyboard	Keyboard	/Mouse Flash

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OVPsim booting Linux on ARM Integrator / ARM926





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OVPsim MIPS Linux platform





0xFFFFFFF		
0x1fd00000	Controller	
0x1fc00000	Flash	
0x1f100000	Controller	
0.11100000	FPGA	
	UART	
0×16000000	FPGA	
0x1e400000		
0x1e000000	Flash	
0,1000000		
0x1be00000	Controller	
0.10000000	PCI	
0x18000000	10	
	PCI	
	Memory	
0x08000000		
	Memory	
	SmartDram	
0x00000000	SmartProm	

- Boot Linux on Windows PC...
- Easy to <u>run</u>: <u>platform</u> .exe vmlinux

OVPsim Heterogeneous Platform ARM Nucleus / MIPS Linux





Software debug on prototype: Run gdbserver on target and Eclipse on host to debug application on target





Remote Target

Using a Virtual Platform from OVP provides exactly the same environment





Demo: Developing Software on Virtual Platforms



- Virtual Platforms are replacing hardware prototypes for software development
 - It's a new world, with familiar tools
 - Virtual Platforms provide significant benefits
 - Software development uses standard tools
 - Eclipse, GDB, gdbserver

Benefits of Virtual Platforms for software development
Easier to deploy, more reliable, deterministic
Often much faster
Easier to use for regression testing, improving quality
Available much sooner (developing SoC takes 2 years of delay...)
With OVP, the models are free, fast, and easy to use...



Thank you

For more information visit www.OVPworld.org